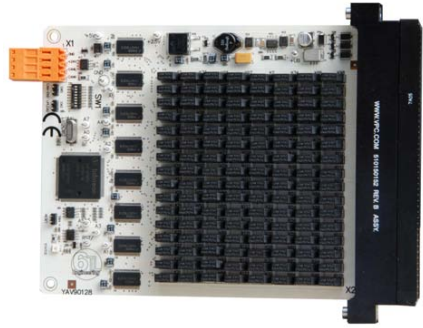


YAV90128

128 channel switching matrix



Features

- > 128 relays organized in 4 lines of 16 and 8 lines of 8, that can be configured as scanner or matrix
- > 0,5A switching
- > 300VDC/300VAC CAT II
- > CAN bus controlled
- > Reliable VPC 90 series I/O connector

Applications

- > Instruments commutation in ATEs

Specifications

> Relay characteristics

Number of channels	128
Relay type	SPST, normally open, nonlatching 100V
Maximum switching voltage AC/DC	0,25A @ 100VAC
Maximum switching Current (Cos phi=1)	0,5A
Peak current	3W
Maximum switching power	250E6 cycles
Mechanical endurance	120mOhm
Contact resistance	0,14pF
Capacity at open contacts	0,2 ms
Switch-on time	0,1 ms
Switch-off time	

> Power supply

Operative voltage range	20..29VDC
Max. 24V current requirement	100mA

> Physical

I/O connector	VPC Quadrapaddle, 96 Position, 510150152
Maximum current per VPC contact	5A
Dimensions mm (HxL)	142x187

> Environment

Operating temperature	0 to 45 °C
Storage temperature	-20 to 70 °C
Relative humidity	10 to 90% relative humidity, noncondensing

Overview

A unique YAV90128 module is able to do all combination of signal commutations at low frequency between the device under test and the measuring instrumentation

It is organized in 4 lines of 16 relays and 8 lines of 8 relays each, with contacts that can be configured as scanner or matrix, individually in each test fixture. This configuration possibility makes it ideal to be used in automatic test systems where maximum flexibility is needed.

Only a 24VDC power supply is needed, wired at the back 4 pin connector together with the CAN bus port.

With its 192 contacts Quadrapaddle connector it has dedicated inputs to automatically identify its position in the test system, in case more modules are mounted.

It has CAN bus control and all the software needed to be used "Plug & Play" in systems based on executive software LabView and TestStand.

The diagram shows 48 pins arranged in four columns (A, B, C, D) of 12 pins each. Column A includes 0V, Pos_ID0, Pos_ID1, and Pos_ID2. Column B includes CAN L, Pos_ID2, Pos_ID3, and Pos_ID4. Column C includes CAN H, Pos_ID4, Pos_ID5, and Pos_ID6. Column D includes +24V, Pos_ID6, Pos_ID7, and Pos_ID8. The pins are connected to a central 'POWER SUPPLY CONTROLLER / ADDRESS' block which has inputs for DA3, DA2, DA1, DA0 (Device address) and FD3, FD2, FD1, FD0 (Device address). The controller also has outputs for 250 KB/s, 125 KB/s, 100 KB/s, and 50 KB/s. At the bottom, there are connections for 5VDC/PCS, 0V, +24V, CAN H, CAN L, Invert B0, Invert B1, and Invert B2.

The software panel shows a grid of 128 channels, organized into four groups of 32 channels each. Each channel has a status indicator (e.g., '0', '1', '2', '3', '4') and a label (e.g., 'A01_A08', 'A09_A16', 'A17_A24', 'A25_A32'). The panel is titled 'Phi6' and 'YAV90128 17'.

> YAV90128 Pin Assignment

> YAV90128 Software Virtual Panel